

EXPEDITED PROCEDURE – EXAMINING GROUP 2186

S/N 10/643,585

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	Steven L. Scott	Examiner:	Sheng Jen Tsai
Serial No.:	10/643,585	Group Art Unit:	2186
Filed:	August 18, 2003	Docket No.:	1376.700US1
Title:	LATENCY TOLERANT DISTRIBUTED SHARED MEMORY MULTIPROCESSOR COMPUTER		

AMENDMENT & RESPONSE UNDER 37 C.F.R. 1.116

Mail Stop RCE
Commissioner for Patents
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In response to the Final Office Action mailed January 25, 2007 and the Advisory Action of April 2, 2007, please amend the application as follows.